

<i>Rpl32</i>	forward	GACGCTTCAAGGGACAGTATCTG
	reverse	AAACGCGGTTCTGCATGAG
<i>Sod1</i>	forward	GTAATTAACGGCGATGCCAAG
	reverse	CATGCAGCCATTGGTGTTG
<i>Jafrac2</i>	forward	CTATTCGTCTGGTTCAGGCC
	reverse	GCATCTCTATCTGTCCCTAGTTG
<i>GstD2</i>	forward	AACAATTGAAGCCGGAGTTTG
	reverse	AACAGATAGTCATCCTTGCCG
<i>Sod2</i>	forward	GTGGCCCGTAAAATTTTCGC
	reverse	TGGTGCTTCTGGTGATGC
<i>Jafrac1</i>	forward	CGAGTTCGCAAGATCAATTG
	reverse	TTCATCGACTTGTCAGCCAG
<i>GstD1</i>	forward	GGAGTTCCTGAAGATCAATCCC
	reverse	GCCGTA CT TCTCCACCAAATA
<i>Catalase</i>	forward	CTGATTCCTGTGGGCAAAATG
	reverse	GTAGGAGAACAGACGACCATG
<i>IRC</i>	forward	GAGAGGGTCACTATGCTTTCAG
	reverse	AATGTGATCGGGCTCATAACAG